## **ABSTRACT**

A printed circuit board having a first switching device (S1 or S2), a second switching device (S3 or S4), a third switching device (S5 or S6), and a common source node (26 or 166) that are each mounted to a surface of the printed circuit board (50 or 150). The printed circuit board further includes at least a first set of conductive paths in a first layer, a second set of conductive paths in a second layer, and a plurality of vias that connects the first layer to the second layer. The first set of conductive paths provides electrical conductivity between the common source node, the first switching device, the second switching device, and third switching device. The second set of conductive paths in the second layer provides electrical conductivity between the common source node, the first switching device, and the third switching device. The physical distance between the first low side switching device and the common source node is greater than a distance between the second low side switching device and the common source node.

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